



# **Intel® Desktop Board D875PBZ Specification Update**

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Order Number: C40099-001

The Intel® Desktop Board D875PBZ may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D875PBZ may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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## REVISION HISTORY

Date of Revision	Version	Description
April 14, 2003	-001	This document is the first Specification Update for the Intel® Desktop Board D875PBZ. Added Specification Clarification 1.

## PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D875PBZ Technical Product Specification* (Order number C31765). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D875PBZ. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 875P Chipset: 82875P Memory Controller Hub (MCH) Specification Update* (Order Number TBD) for specification updates concerning the 82875P MCH Controller and that may apply to the desktop board D875PBZ. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 82801ER I/O Controller Hub 5 (ICH5) Specification Update* (Order Number TBD) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D875PBZ. Unless otherwise noted in this document, it should be assumed that any ICH 5 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Nomenclature

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board D875PBZ's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

# **Specification Update for the Intel® Desktop Board D875PBZ**





## GENERAL INFORMATION

**Basic Desktop Board D875PBZ Identification Information**

AA Revision	BIOS Revision	Notes
C27085-204	BZ87510A.86A.0019.P02	1-5
C26680-204	BZ87510A.86A.0019.P02	1-5

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82875P Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82875P MCH	A2	SL744
82801ER ICH	A3	SL742

3. Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Pentium 4 processor and that may apply to the desktop board D875PBZ.
4. Refer to the *Intel® 875P Chipset: 82875P Memory Controller Hub (MCH) Specification Update* (Order Number TBD) for errata related to the 82875P MCH that may apply to the desktop board D875PBZ.
5. Refer to the *Intel® 82801ER I/O Controller Hub 5 (ICH5) Specification Update* (Order Number TBD) for errata related to the 82801ER I/O Controller Hub that may apply to the desktop board D875PBZ.

## Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the desktop board D875PBZ. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
		There are no characterized errata for this product.
NO.	PLANS	Specification Clarification
1	Doc	Add note that specifies Enhanced Mode IDE/Serial ATA requirements to Section 3.3.2, PCI IDE Support.

## SPECIFICATION CLARIFICATION

The Specification Clarifications listed in this section apply to the *Desktop Board D875PBZ Technical Product Specification* (Order Number C31765). All Specification Clarifications will be incorporated into a future version of that specification.

- 1. Add Note That Specifies Enhanced Mode IDE/Serial ATA Requirements to Section 3.3.2, PCI IDE Support.**

### NOTE

*The Enhanced mode IDE/Serial ATA BIOS option requires support for resources up to a maximum six devices, and has been tested with Windows 2000 and Windows XP. This BIOS option should be set to Legacy mode when used with operating systems that support a maximum of two IDE channels (four devices).*