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**DATE:** July 30, 2004  
**PRODUCT:** D915GEV/D915GUX Standard BIOS

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**Build 0260**

**About This Release:**

- July 30, 2004
- EV91510A.86A.0260.2004.0730.1128
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell UNDI 2.03
- CPU Patches: M04f251f, M0df320a, M0df330b and M1df340e

**New Fixes/Features:**

- Fixed rear chassis fan fluctuation after resuming from S3.

**Build 0255**

**About This Release:**

- July 28, 2004
- EV91510A.86A.0255.2004.0728.1104
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell UNDI 2.03
- CPU Patches: M04f251f, M0df320a, M0df330b and M1df340e

**New Fixes/Features:**

- Fixed memory test failure with 1GB memory installed and IGD enabled.
- Fixed S3 resume issue with ICH6-B2 stepping.
- Added BTX BMI support for SMSC H6E and ADI H6 devices.
- New BMI Sequence for Heceta6 E support.

**Build 0244**

**About This Release:**

- July 15, 2004
- EV91510A.86A.0244.2004.0715.1600
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell UNDI 2.03
- CPU Patches: M04f251f, M0df320a, M0df330b and M1df340e

**New Fixes/Features:**

- Updated Marvell PXE code to version 2.05.
  - Fixed 1066 FSB and Micron DDR2-400 DIMM related POST Code 0D5h hangs.
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- Updated Video Addin code with more dynamic algorithm per primary and secondary video operational models.
- Updated to Rev 1.30 925 MRC+ and latest BIOS spec update 1.30 as well. Resulted in a correction to the REOST settings for 925 chipsets that has not as of yet been published in the BIOS Spec docs.
- Fixed corruption seen in "El-Torito" CD boot menu.
- Fixed the issue of not being able to boot to SATA ATAPI drives when RAID is enabled.
- Added a warning message in POST if 2x12 or 1x4 power connector is not plugged into the system.
- Fixed an issue of not detecting Marvell LAN on D915GMH board.
- Added soft-reset of TPM module during POST to resolve issue where TPM intermittently disappears during manufacturing.

#### **Build 0234**

##### **About This Release:**

- June 29, 2004
- EV91510A.86A.0234.2004.0629.1028
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell UNDI 2.03

##### **New Fixes/Features:**

- Fixed issue where system would not complete POST if no video card plugged in and a LAN card present.
- Fixed issue where system was unable to PXE boot if the 3ware 7506-4 PCI IDE card is installed.
- Changed to default LED state of PA3.0 from yellow to green.
- Updated NS SIO GIPIO programming.
- Updated Video BIOS to Build 3362.

#### **Build 0213**

##### **About This Release:**

- June 09, 2004
- EV91510A.86A.0213.2004.0609.1722
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10
- Marvell UNDI 2.03

##### **New Fixes/Features:**

- Fixed an issue of losing Audio after resuming from S3.

#### **Build 0211**

##### **About This Release:**

- June 04, 2004
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- EV91510A.86A.0211.2004.0604.1616
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10
- Marvell UNDI 2.03

**New Fixes/Features:**

- Added support for NX and PSMI.
- Added a workaround to enable Marvell LAN when CMOS is corrupt.

**Build 0209**

**About This Release:**

- May 26, 2004
- EV91510A.86A.0209.2004.0526.1812
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10
- Marvell UNDI 2.03

**New Fixes/Features:**

- Updated BMI sequence for spike smoothing for Heceta6e.
- Fixed issues with 925 PAT2 setting related to REOST/RCVEN and FSB Data Bypass as well as Scheduler Stage Mask init for DRC2 reg.
- Changed sleep state LED to steady Yellow.
- Updated Marvell PXE code to version 2.03

**Build 0200**

**About This Release:**

- May 25, 2004
- EV91510A.86A.0200.2004.0525.2022
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10

**New Fixes/Features:**

- Corrected MCH and ICH programming to run Azalia audio with PCI Express Virtual Channel 1.
- Added workaround for ICS clock to turn on PCI clocks.
- Enabled spike smoothing for Heceta6e.

**Build 0197**

**About This Release:**

- May 24, 2004
  - EV91510A.86A.0197.2004.0524.1719
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- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10

#### **New Fixes/Features:**

- Fixed the problem where USB HDD greater than 8.4GB was always recognized as 8.4GB under DOS.
- Updated Audio Verb tables.
- Fixed add-in video hang when running 3DMark03
- Added WIN XP SP1 workaround for ich6 USB2.0 handoff hang when system bios uses USB 2.0 legacy for boot devices.
- Fixed issue where system sometimes "bounces" out of S3.
- Added a workaround for ICS clock hang at POST code 38.

<b>Build 0175</b>
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#### **About This Release:**

- May 16, 2004
- EV91510A.86A.0175.2004.0516.1341
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10

#### **New Fixes/Features:**

- Modified ICH-related strings to use ICH6.
  - Fixed an issue of WinXP\* reporting a device failure error after S3 resume when Aperture size is set to 128MB.
  - Added AHCI Setup option for ICH6R when RAID is disabled and one desires to use AHCI mode.
  - Fixed front panel LED functionality when going to the S1 ACPI sleep state.
  - Updated to latest Production VBIOSs for 915 chipset. Build 3329 dated 4-12-04
  - Added OS-Driver related BIOS Setup options to Video Config Page to allow memory allocated by OS to Driver to be flexible per 915 DVMT 3.0 White Paper Rev 1.0.
  - Fixed SMBIOS memory type structure data inconsistencies between types 16 thru 20 and also supplemental type 187.
  - Fixed issue where GMCH SMI calls return incorrect values when not running in protected-mode.
  - Turned off unused PCI clocks.
  - Fixed incorrect SMBIOS type 4 Processor Upgrade info for Socket-T processor.
  - Added support for multiple capsule updates in FLASH5.3.
  - Fixed hang in BIOS Setup when displaying Hardware Monitoring page with certain PCI cards installed.
  - Added more ACPI S3 save restore items for PCIe devices.
  - Update Memory init code to add PAT2 capabilities programming for 915/925 chipsets per chipset POR definitions.
  - Completely correct DOS and other non-ACPI aware OS's problems with Power Button momentary Soft-Off operation as well as iFlash.exe and iFlash2.exe update hangs under DOS.
  - Added items for DMI link retrain and DRC setting defaults force.
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- 915-925 VC updates per BIOS specs 1.0 (for ich6 as well.)
- Updated video detection code to fix check point 2A hangs.
- Enabled TM2 only for F40h and above CPUs.
- Added chipset programming update for ICH6 BIOS Specification Update.
- Corrected S3 restore map for Port Angeles 1.6 compliant SIO devices.

<b>Build 0132</b>
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**About This Release:**

- April 30, 2004
- EV91510A.86A.0132.2004.0430.1759
- Core Baseline version 01.07.00
- VBIOS build 3305
- UNDI 4.1.10

**New Fixes/Features:**

- Fixed incorrect and repeated programming of CPU cachability regions in early BIOS code, which caused problems later when properly programmed.
  - Fixed issue where processor Thermal Monitoring II and Thermal Monitoring were enabled at the same time.
  - Fixed issue of not being able to shut down the system if wake on LAN from S5 set to enabled.
  - Fixed issues in Virtual-86 mode with IFLASH, ITOOLKIT, and BEL I15MFG tests.
  - Updated MRC to 0.83.
  - Added support for new versions of the ST Micro 4Mbit FWH.
  - New BMI Sequence for Heceta6 E support.
  - Fixed PCI Express add-in slot build options.
  - Added SMBIOS support for PCI Express slots.
  - Skipped clearing IA32\_MCi\_STATUS MSRs in BIOS.
  - Fixed memory controller and memory module related failures in BEL SMDXREF test.
  - Fixed BEL PCITEST failures with IRQ in PCI configuration space not in IRT bitmap.
  - Fixed intermittent hang during POST at PCI reset or post code 2Ah.
  - Enabled SATA HPCP bit in AHCI space for SATA Hot-plug support by OS drivers.
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