



Intel Corporation, 5200 NE Elam Young Parkway
Hillsboro, OR 97124-6497

DATE: September 24, 2004
PRODUCT: D915GAG / D915GAV / D915GEV / D915GHA / D915GRF / D915GUX
Standard BIOS

Build 0283

About This Release:

- September 24, 2004
- EV91510A.86A.0283.2004.0924.1109
- Core Baseline version 01.07.00
- VBIOS build 3398
- UNDI 4.1.10
- Marvell UNDI 2.05

New Fixes/Features:

- Fix for boot order being reordered when a USB drive is inserted and then removed while the Floppy Drive is set to Disabled.
- Fixed an issue with L2 cache size display for some processors.
- Fixed the issue when High-Definition Audio is non-functional after S3 resume.
- Fixed display of Celeron® Badge with Celeron® CPU's.
- Fixed an issue of intermittent hang when using DDR1 400 2.6V DIMMs.
- Added ASF support for non-Intel LAN vendors.
- Added DDR2 533 MHz 3-3-3 1.9V Auto-Detect code.
- Changed the default splash image name to "Default BIOS POST Image".

Build 0276

About This Release:

- September 10, 2004
- EV91510A.86A.0276.2004.0910.1659
- Core Baseline version 01.07.00
- VBIOS build 3398
- UNDI 4.1.10
- Marvell UNDI 2.05
- CPU Patches: M10F2520, M0df320a, M0df330b and M1df3413

New Fixes/Features:

- Fixed the issue, where system hangs with SCSI card and SCSI drive installed on D915G Development Platforms with support for TSEG SMI handler.
- Fixed the issue with L2 cache size display for some processors.
- Fixed an issue with extended POST times.
- Updated 915G Video BIOS to Build 3398.
- Fixed an issue of Microsoft Windows XP hanging after installing LAN Driver on D915GMH board.
- Fixed test hang when using 2GB memory configuration.

- Implemented ICH6 BIOS spec update 1.6 SATA programming change, section #1. Also implemented SATA SRST Errata BIOS workaround.

Build 0270

About This Release:

- September 1, 2004
- EV91510A.86A.0270.2004.0901.1405
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Added feature to enable password cloning for Integrator Tool Kit.
- Updated system security by increasing length of supervisor and user passwords from 7 to 16 characters and moving the storage location of user and supervisor passwords out of system CMOS.
- Added a switch in BIOS Setup to disable all USB ports while in Maintenance mode.
- Changed the default DVMT size to 128MB instead of 64MB.
- Changed to not waiting for keyboard while in manufacturing mode if 2x12 or 1x4 power connector is not plugged into the system.
- Fixed issue where devices installed in PCI Express* Slots do not function.
- Added workaround for S3/S4 transition issue.
- Fixed POST code 38h hang when installing USB Card Reader.
- Added workaround for ATI* PCI Express RV423 hang when running 3DMark03.
- Fixed system hang when executing Integrator Tool Kit in VM86 mode.
- Fixed the problem where BIOS could not program Hardware Monitor chip with certain processor revisions.
- Fixed manufacturing issue where POST cards on some boards would stop displaying at POST code DAh.
- Updated the pulse width stretching sequence in POST.
- Fixed 1394 devices lost after resuming from S3.

Build 0260

About This Release:

- July 30, 2004
- EV91510A.86A.0260.2004.0730.1128
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Fixed rear chassis fan fluctuation after resuming from S3.

Build 0255

* Other names and brands may be claimed as the property of others.

About This Release:

- July 28, 2004
- EV91510A.86A.0255.2004.0728.1104
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Fixed memory test failure with 1GB memory installed and on-board graphics.
- Fixed S3 resume issue with certain chipset revision.
- Added BTX BMI support for SMSC* and ADI* Precision Cooling devices.
- Updated BMI Sequence for Precision Cooling support.

Build 0244**About This Release:**

- July 15, 2004
- EV91510A.86A.0244.2004.0715.1600
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Updated Marvell* PXE code to version 2.05.
- Fixed POST Code 0D5h hang issues with certain DIMMs.
- Updated Video Add-in code with more dynamic algorithm per primary and secondary video operational models.
- Fixed corruption seen in "El-Torito" CD boot menu.
- Fixed the issue of not being able to boot to SATA ATAPI drives when RAID is enabled.
- Added a warning message in POST if 2x12 or 1x4 power connector is not plugged into the system.
- Resolved issue where TPM is disappears intermittently.

Build 0234**About This Release:**

- June 29, 2004
- EV91510A.86A.0234.2004.0629.1028
- Core Baseline version 01.07.00
- VBIOS build 3362
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Fixed issue where system would not complete POST if no video card plugged in and a LAN card present.
- Fixed issue where system was unable to PXE boot if certain PCI IDE controller cards are installed.
- Changed default LED state from yellow to green.

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- Updated SIO GPIO programming.
- Updated Video BIOS to Build 3362.

Build 0213

About This Release:

- June 09, 2004
- EV91510A.86A.0213.2004.0609.1722
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Fixed an issue of losing audio after resuming from S3.

Build 0211

About This Release:

- June 04, 2004
- EV91510A.86A.0211.2004.0604.1616
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10
- Marvell UNDI 2.03

New Fixes/Features:

- Added support for NX and PSMI.
- Added a workaround to enable LAN when CMOS is corrupt.

Version 0209

About This Release:

- May 26, 2004
- EV91510A.86A.0209.2004.0526.1812
- Core Baseline version 01.07.00
- VBIOS build 3329
- UNDI 4.1.10

New Fixes/Features:

- Initial Release