



BIOS ENGINEERING

Release Notes

DATE: August 25, 2004
PRODUCT: D925XCV and D925XBC Standard BIOS

Build 0249

About This Release:

- August 19, 2004
- CV92510A.86A.0249.2004.0819.1259
- Core Baseline version 01.07.00
- PXE v2.05
- UNDI 4.1.10

New Fixes/Features:

- Added board ID's for newly added SKU's.
- Fixed issue where some devices installed in PCI Express Slots did not function.
- Changed the default DVMT size to 128MB instead of 64MB.
- Fixed rear chassis fans fluctuation after resuming from S3.
- Fixed manufacturing issue where POST cards on some boards would stop displaying at POST code DAh.
- Fixed S3 resume issue with ICH6-B2 stepping.
- Added warning message in POST if 2x12 or 1x4 power connector is not plugged into the system.
- Added fix for S3/S4 transition issue.
- Fixed issue where memory timings set by IDCC were not being programmed into the BIOS.
- Updated Video Add-in code with a more dynamic algorithm per primary and secondary video operational models.
- Fix and issue where a 3rd party video game would hang when running a popular 03 benchmark.
- Fixed issue with a specific keyboard.

Build 0218

About This Release:

- Jun 24, 2004
- CV92510A.86A.0218.2004.0709.1458
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:

- Fixed the issue of not being able to boot to SATA ATAPI drives with RAID enabled.
 - Added soft-reset of Infineon TPM during POST to resolve issue where TPM intermittently disappears during manufacturing.
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Build 0210

About This Release:

- Jun 24, 2004
- CV92510A.86A.0210.2004.0624.1545
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:

- Fixed the issue of memory timings set by IDCC not getting programmed into the BIOS.
- Fixed the issue of ECC setup option not being displayed in BIOS setup when ECC DIMM is in use.

Build 0200

About This Release:

- Jun 21, 2004
- CV92510A.86A.0200.2004.0621.1438
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:

- Updated memory information related for IDCC (aka: ETK) BIOS support
- Fixed issue where system would not complete POST if no video card plugged in and a LAN card present (1629).
- Fixed issue where system was unable to PXE boot if the a particular PCI IDE card is installed (1519).
- Fixed an issue where system will not boot to PXE when certain 256 MB AGP cards are present (1470). This also fixed the PXE boot issue with certain PCIe graphics cards.
- Moved a processor configuration switch to runtime code so that that certain processor capabilities can be retrieved properly.

Build 0193

About This Release:

- Jun 09, 2004
- CV92510A.86A.0193.2004.0609.1843
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:

- Correctly display AUX Power option for the right SKU
 - Fixed an issue of losing Audio after resuming from S3.
 - BIOS Identification string updated to the following new format:
EXAMPLE:
CV92510A.86A.0006.2004.0121.1203
BUILD NUMBER = 0006
YEAR = 2004
MONTH DAY = 01 21
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HOUR MINUTE = 12 03

