intel®

BIOS ENGINEERING
Release Notes

DATE: June 17, 2005
PRODUCT: D925XCV / D925XBC / D925XECV2 / D925XEBC2 Standard BIOS

Build 0463

About This Release:
- June 17, 2005
- CV92510A.86A.0463.2005.0616.1851
- Core Baseline version 01.07.00
- Yukon PXE v3.03
- UNDI 4.1.16

New Fixes/Features:
- Updated new CPU microcode.
- Fixed issue for module changes not updated with Intel® Integrator Toolkit (ITK Tool) when BIOS flashing with Intel® Express BIOS Update (EBU).
- Fixed RPM logo not able to update issue.
- Fixed On Screen Branding (OSB) logo not displayed when system wake from hibernation.
- Fixed ECC error not log in Event Log issue.
- Fixed issue where Event log is not marked as read after user selecting "Mark event log as read" in the BIOS Setup Page.
- Set PCIe "Compliance Test Pattern" to enable for manufacturing mode.
- Workaround destroy and recovery issue.
- Set ECC support to default enables.
- Updated video BIOS to version 1216.
- Updated SMBIOS support for PCI Express slot per SMBIOS 2.4 Specification.
- Corrected badge placement when badge is displayed with .BMP logo files.
- Fixed "protected CMOS data" being cleared in manufacturing mode.
- Fixed Enhanced Intel Speedstep® Technology (EIST) not able to step down when using 3.0GHz CPU.
- Fixed Wake on LAN (WOL) not functioning issue on specific LAN vendor.
- Removed workaround for certain Graphic card.
- Optimizing F000 segment.
- Updated with new audio verb table.

Build 0446

About This Release:
- April 28, 2005
- CV92510A.86A.0446.2005.0428.1154
- Core Baseline version 01.07.00

- Yukon PXE v3.03
- UNDI 4.1.16

New Fixes/Features:
- Add update to initialize the Heceta6 fan control after boot block to provide better user experience by reducing the duration of a fan spin at full speed.
- Fixed bugs for Trusted Computing Platform Alliance (TCPA).
- Special workaround for a specific PCIe 4-port Video card.
- Fixed issue of "keyboard not functional" error being logged in BIOS setup event log when USB keyboard exists in the system.

Build 0431

About This Release:
- March 28, 2005
- CV92510A.86A.0431.2005.0328.1556
- Core Baseline version 01.07.00
- Yukon PXE v3.03
- UNDI 4.1.16

New Fixes/Features:
- Updated to latest Intel® Application Accelerator ICH6R SATA RAID OPROM code. Production Version 4.0.2.6810
- Eliminate a specific graphic cards workaround when running PC benchmarking test. It saves 128MB more memory available for Operating System.
- Fixed an issue where CD recovery failed on ICH6 RAID
- Fixed an issue where Power Button override when going into S3 caused system to hang on resume.

Build 0404

About This Release:
- January 26, 2005
- CV92510A.86A.0404.2005.0126.2132
- Core Baseline version 01.07.00
- LAN PXE v3.03
- UNDI 4.1.16

New Fixes/Features:
- Added support for future unreleased processors.
- Updated Express BIOS Update (EBU) packager to 2.05.08.
- Fixed issue where the USB 2.0 Legacy Support BIOS Setup option was not visible in ITK.
- Added BIOS support for Enhanced Intel® Speedstep Technology.
- Removed 667 MHz option within Memory Frequency Setup selection.
- Added SMBIOS structure to support Intel Stable Image Platform Program.
- Fixed incorrect memory sizes displayed during POST.
- Fixed issue where recovery was failing from ATAPI devices.
- Fixed an issue of CPU temperature too high in BIOS hardware monitoring.
- Fixed an issue of corrupting INT3 vector on a non-AHCI ICH6 board.
- Added ASF support for non-Intel LAN vendors.
- Fixed issue where Hard Drive size was incorrect after using F12 PXE Boot key during POST.
- Fixed issues with 2X PCI memory and I/O space allocations.
- Reduced S3 Resume and POST times.

Build 0338

About This Release:
- October 13, 2004
- CV92510A.86A.0338.2004.1012.1817
- Core Baseline version 01.07.00
- LAN PXE v3.03
- UNDI 4.1.10

New Fixes/Features:
- Fixed issues with Extended Burn-in Mode.
- Updated MFG Default for USB Boot to Disabled per latest DFT.
- Updated LAN PXE code to version 3.03.
- Fixed automatic-reboot condition seen in only in manufacturing.
- Fixed incorrect memory voltage condition after AC power loss.
- Modified Burn-in Mode strings.
- Changed the default splash image name to "Default BIOS POST Image".

Build 0316

About This Release:
- October 1, 2004
- CV92510A.86A.0316.2004.0929.1610
- Core Baseline version 01.07.00
- LAN PXE v2.05
- UNDI 4.1.10

New Fixes/Features:
- Fixed the issue where system hangs with SCSI card and SCSI drive installed.
- Fixed an issue of WinXP hang when only a USB keyboard is attached to the system.

Build 0313

About This Release:
- September 23, 2004
- CV92510A.86A.0313.2004.0923.1012
- Core Baseline version 01.07.00
- LAN PXE v2.05
- UNDI 4.1.10

New Fixes/Features:
- Added Extended Burn-In Mode support and POST warning message.

Build 0249

About This Release:
- August 19, 2004
- CV92510A.86A.0249.2004.0819.1259

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* Other names and brands may be claimed as the property of others

- Core Baseline version 01.07.00
- PXE v2.05
- UNDI 4.1.10

New Fixes/Features:
- Added board ID's for newly added SKU's.
- Fixed issue where some devices installed in PCI Express Slots did not function.
- Changed the default DVMT size to 128MB instead of 64MB.
- Fixed rear chassis fans fluctuation after resuming from S3.
- Fixed manufacturing issue where POST cards on some boards would stop displaying at POST code DAh.
- Fixed S3 resume issue with ICH6-B2 stepping.
- Added warning message in POST if 2x12 or 1x4 power connector is not plugged into the system.
- Added fix for S3/S4 transition issue.
- Fixed issue where memory timings set by IDCC were not being programmed into the BIOS.
- Updated Video Add-in code with a more dynamic algorithm per primary and secondary video operational models.
- Fix and issue where a 3rd party video game would hang when running a popular 03 benchmark.
- Fixed issue with a specific keyboard.

Build 0218

About This Release:
- Jun 24, 2004
- CV92510A.86A.0218.2004.0709.1458
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:
- Fixed the issue of not being able to boot to SATA ATAPI drives with RAID enabled.
- Added soft-reset of Infineon TPM during POST to resolve issue where TPM intermittently disappears during manufacturing.

Build 0210

About This Release:
- Jun 24, 2004
- CV92510A.86A.0210.2004.0624.1545
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:
- Fixed the issue of memory timings set by IDCC not getting programmed into the BIOS.
- Fixed the issue of ECC setup option not being displayed in BIOS setup when ECC DIMM is in use.

Build 0200

About This Release:
- Jun 21, 2004
- CV92510A.86A.0200.2004.0621.1438
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:
- Updated memory information related for IDCC (aka: ETK) BIOS support
- Fixed issue where system would not complete POST if no video card plugged in and a LAN card present (1629).
- Fixed issue where system was unable to PXE boot if a particular PCI IDE card is installed (1519).
- Fixed an issue where system will not boot to PXE when certain 256 MB AGP cards are present (1470). This also fixed the PXE boot issue with certain PCIe graphics cards.
- Moved a processor configuration switch to runtime code so that certain processor capabilities can be retrieved properly.

Build 0193

About This Release:
- Jun 09, 2004
- CV92510A.86A.0193.2004.0609.1843
- Core Baseline version 01.07.00
- LAN PXE v2.03
- UNDI 4.1.10

New Fixes/Features:
- Correctly display AUX Power option for the right SKU
- Fixed an issue of losing Audio after resuming from S3.
- BIOS Identification string updated to the following new format:

  EXAMPLE:
  CV92510A.86A.0006.2004.0121.1203
  BUILD NUMBER = 0006
  YEAR = 2004
  MONTH DAY = 01 21
  HOUR MINUTE = 12 03