



# **Intel<sup>®</sup> Desktop Board D945PLM Specification Update**

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Order Number: D23995-001US

The Intel<sup>®</sup> Desktop Board D945PLM may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D945PLM may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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## REVISION HISTORY

Date of Revision	Version	Description
May 26, 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D945PLM.

## PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D945PLM Technical Product Specification* (Order number D14071). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D945PLM. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 945 Chipset Specification Update* (Order Number TBD) for specification updates concerning the 82945 MCH Controller and that may apply to the desktop board D945PLM. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® I/O Controller Hub 7 (ICH7) Family EDS Specification Update* (Order Number TBD) for specification updates concerning the 82801GB ICH7 and 82801GR ICH7R I/O Controller Hub and that may apply to the desktop board D945PLM. Unless otherwise noted in this document, it should be assumed that any ICH 7 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Nomenclature

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board D945PLM's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the  
Intel® Desktop Board D945PLM**





## GENERAL INFORMATION

**Basic Desktop Board D945PLM Identification Information**

AA Revision	BIOS Revision	Notes
C98851-301	SN94510J.86A.0028	1-5

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82945P Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82945P MCH	A2	SL8HT
82801GB ICH7	A1	SL8FX
82801GR ICH7R	A1	SL8FY

3. Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Intel Pentium 4 processor and that may apply to the desktop board D945PLM.
4. Refer to the *Intel® 945P Chipset Specification Update* (Order Number TBD) for errata related to the 82945P MCH that may apply to the desktop board D945PLM.
5. Refer to the *Intel® I/O Controller Hub 7 (ICH7) Family EDS Specification Update* (Order Number TBD) for errata related to the 82801GB I/O Controller Hub that may apply to the desktop board D945PLM.

## Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D945PLM. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
		There is not characterized erratum for this product